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APPLICATION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO	CONFIRMATION NO
09 941,612	08 30 2001	Yoshinobu Aoyagi	1794-0141P	6758

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EXAMINER

SONG, MATTHEW J

ART UNIT	PAPER NUMBER
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1765

DATE MAILED 05 23 2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Application No.

Applicant(s)

09 941,612

AOYAGI ET AL

Office Action Summary

Examiner

Art Unit

Matthew J Song

1765

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 March 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 4-28 is/are pending in the application.
- 4a) Of the above claim(s) 22-25 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 4-21 and 26-28 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s) _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ 6) ☐ Other

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claims 26-28 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 26 recites, "wherein said plural types of impurity raw materials form impurity pairs within at least one of said crystal raw materials" in lines 7-8. It is unclear how the supply of impurity materials will affect the crystal raw materials, the crystal layer would be affected not the crystal raw materials, likewise for claims 27 and 28.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 4-21 and 26-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nishizawa et al (US 5,693,139) in view of Edmond et al (US 5,739,554).

Nishizawa et al discloses a method of growing doped semiconductor monolayers, note entire reference, comprising raw material gases of Gallium (Ga) and Arsenic (As), where Ga is supplied for 0.5 to 10 seconds, the chamber is evacuated, this reads on applicant's purged for a predetermined time, and As is supplied for 2 to 200 seconds and the cycle is repeated (col 7, ln 1-67; col 8, ln 1-30 and Fig 7B and Fig 11). Nishizawa et al also discloses a p-type layer is formed by introducing an impurity gases and Ga simultaneously but alternately with an As source, where the impurity gas is an Mg, Zn or Cd containing gas or Silane. Nishizawa et al also discloses a n-type layer doped with Se or S and the impurity gas is introduced cyclically with the Ga gas and As gas or the impurity gas and Ga gas are introduced simultaneously but alternately with the As gas (col 8, ln 31-60). Nishizawa et al also discloses forming pnp bipolar transistors (col 8, ln 61-67). Nishizawa et al also discloses nozzles 44, 45 and 46 for introducing gaseous compounds used for impurity doping for introducing group II, IV and VI gases (col 10, ln 50-67). Nishizawa et al also discloses different modes of doping, where the dopant is added at the exhaustion of an As gas, the introduction of a Ga gas, the exhaustion of a Ga gas or at the introduction of As gas (col 11-13 and Fig 11). Nishizawa et al also discloses other III-V semiconductors are applicable to the invention (col 14, ln 5-55). Nishizawa et al also discloses introduction of a Ga source gas and a group II dopant simultaneously to form a p-type layer (col 8, ln 30-45) and the introduction of a group IV dopant after the introduction of a Ga source gas (col 15, ln 5-50). Nishizawa et al also discloses selection of the timing of doping with respect of the source gas introduction is based on the desired dopant type for the monolayer being grown (col 15, ln 45-55).

Nishizawa et al does not disclose the given time for supplying each of the impurity raw materials are close to each other.

Edmond et al teaches a gallium nitride (GaN) layer co-doped with both a Group II acceptor and Group IV donor (col 4, ln 50-67), where the group II acceptors include Zn or Mg and the Group IV donors include Si or Ge (col 6, ln 20-50), this reads on applicant's the time for supplying each of the impurity raw materials are close to each other. Edmond et al also discloses the GaN layer is formed by CVD, where Trimethylgallium (TMG), ammonia, silane and biscyclopentadienyl magnesium, (Cp)₂Mg are used as reactant gases (col 7, ln 45-67 and col 8, ln 1-50). It would have been obvious to a person of ordinary skill in the art at the time of the invention to modify Nishizawa et al with Edmond et al to form a co-doped GaN layer useful as an active layer (Abstract).

The combination of Nishizawa et al and Edmond et al is silent to forming impurity pairs within at least one of the crystal raw materials. The combination of Nishizawa et al and Edmond et al teach supplying similar raw materials and impurities, as applicant. Also, the combination of Nishizawa et al and Edmond et al teach a similar method of supplying raw materials, purging and supplying impurities, as applicant. Therefore, the plural types of impurity raw forming impurity pairs within at least one of the crystal raw materials is inherent to the invention taught by the combination of Nishizawa et al and Edmond et al.

Referring to claim 4, the combination of Nishizawa et al and Edmond et al teaches forming a co-doped GaN layer using Mg and Si dopant, where the compound semiconductor layer is grown in monolayer by alternate introduction of source gases and the chamber being evacuated continuously throughout the whole method ('139 col 3, ln 35-45) and the Si is

Art Unit: 1765

introduced after the Ga source gas to act as a donor and a Ga source and a Mg dopant are introduced simultaneously but alternately with a As source.

Referring to claim 5, the combination of Nishizawa et al and Edmond et al teaches the introduction of a group IV dopant after the introduction of Ga and prior to the introduction of As and the introduction of group II dopant after the introduction of Ga and prior to the introduction of As (col 13, ln 10-35 and Fig 11).

Referring to claim 6-10, the combination of Nishizawa et al and Edmond et al teaches Ga as a first raw material gas and As or N as a second raw material gas.

Referring to claim 11-20, the combination of Nishizawa et al and Edmond et al teaches a co-doped layer with p-type and n-type impurities.

Referring to claim 21, the combination of Nishizawa et al and Edmond et al teaches using a silane dopant gas. The combination of Nishizawa et al and Edmond et al does not teach using a TESI dopant gas. It would have been obvious to a person of ordinary skill in the art at the time of the invention to modify the combination of Nishizawa et al and Edmond et al by substituting TESI gas for silane because TESI is an equivalent Si source for doping and substituting equivalents for the same purpose is obvious (MPEP 2144.06).

5. Claim 21 is rejected under 35 U.S.C. 103(a) as being unpatentable over Nishizawa et al (US 5,693,139) in view of Edmond et al (US 5,739,554) as applied to claims 4-21 and 26-28 above, and further in view of Manabe et al (US 6,472,690).

The combination of Nishizawa et al and Edmond et al teaches all of the limitations of claim 21, as discussed previously, including using silane as a Si dopant. The combination of Nishizawa et al and Edmond et al does not teach supplying TESI

In a method of forming a gallium nitride compound semiconductor, note entire reference, Manabe et al teaches forming an n⁺ type Gallium nitride layer, using silane or tetraethysilane (TESi) (Example 4). It would have been obvious to a person of ordinary skill in the art at the time of the invention to modify the combination of Nishizawa et al and Edmond et al with Manabe et al because substituting known equivalents for the same purpose is obvious (MPEP 2144.06).

Response to Arguments

6. Applicant's arguments with respect to claims 4-21 and 26-28 have been considered but are moot in view of the new ground(s) of rejection.

7. Applicant's argument that the combination of Nishizawa et al and Edmond et al does not teach forming impurity pairs (pg 11) has been noted but has not been found persuasive. The combination of Nishizawa et al and Edmond et al is silent to forming impurity pairs within at least one of the crystal raw materials, however this would be inherent to the combination of Nishizawa et al and Edmond et al. The combination of Nishizawa et al and Edmond et al teach supplying similar raw materials and impurities, as applicant. Also, the combination of Nishizawa et al and Edmond et al teach a similar method of supplying raw materials, purging and supplying

Art Unit: 1765

impurities, as applicant. Therefore, the plural types of impurity raw forming impurity pairs within at least one of the crystal raw materials is inherent to the invention taught by the combination of Nishizawa et al and Edmond et al. Furthermore, applicants state in the response filed on 3/10/2003 that the concept of impurity pairs is based on the theory of co-doping, note page 9, and the combination of Nishizawa et al and Edmond et al teach co-doping with impurity raw materials (US 5,554 col 3, ln 50-65).

In response to applicant's argument that the combination of Nishizawa et al and Edmond et al is silent to forming impurity pairs, the fact that applicant has recognized another advantage which would flow naturally from following the suggestion of the prior art cannot be the basis for patentability when the differences would otherwise be obvious. See *Ex parte Ohiaya*, 227 USPQ 58, 60 (Bd. Pat. App. & Inter. 1985).

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Daly (US 5,231,298) teaches co-doping GaAs and co-doping improves the performance of the resultant device, effects alloy hardening, inhibits defect formation, improves mobility and reduces the energy gap (col 3, ln 1-20).

Goetz et al (US 6,441,393) teaches a nitride layer is co-doped using co-dopants of Si, Ge, Si, Sn and Ge, Sn to stabilize the structural integrity of the a III-V nitride on a lattice mismatched substrate (col 5, ln 45-65).

Art Unit: 1765

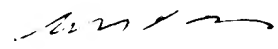
9 Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew J Song whose telephone number is 703-305-4953. The examiner can normally be reached on M-F 9:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Benjamin L Utech can be reached on 703-308-3868. The fax phone numbers for the organization where this application or proceeding is assigned are 703-872-9310 for regular communications and 703-872-9311 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0661.


BENJAMIN L. UTECH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 1700

Application Control Number: 09 941.612

Page 9

Art Unit: 1765

Matthew J Song
Examiner
Art Unit 1765

MJS
May 21, 2003